

**REMARKS**

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Office Action dated July 5, 2007 has been received and its contents carefully reviewed.

By this Amendment, claims 1 and 2 are amended. Accordingly, claims 1-31 are currently pending, of which claims 8-31 are withdrawn from consideration. Reexamination and reconsideration of the pending claims is respectfully requested.

In the Office Action, claims 1-7 are objected to because of informalities. Applicants respectfully submit that these objections are now believed to be moot in view of the amendments in claims 1 and 2.

In addition, claims 1-3 are rejected under 35 U.S.C. § 102(b) as being anticipated by Komiya (U.S. Patent Application Publication No. 2002/0158587); and claims 4-7 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Komiya in view of Morosawa (U.S. Patent Application Publication No. 2006/0139251). These rejections are respectfully traversed and reconsideration is requested.

Claim 1 is allowable over the cited references in that claim 1 recites a combination of elements including, for example, "... driving thin film transistors TFT connected between the EL cells and compensation voltage supply lines that control the current applied to the EL cells; and a bias switch, connected between the N-1th compensation voltage supply line and a control terminal of the driving TFT connected to the Nth compensation voltage supply line that applies a bias voltage to the driving TFT when a scan pulse is supplied to the N-1th gate line...." None of the cited references, singly or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicants respectfully submit that claim 1, and claims 2-7, which depend therefrom, are allowable over the cited references.

On page 3 of the Office Action, the Examiner cites TFT2 in Fig. 5 of Komiya as teaching the driving thin film transistors connected between the EL cells and compensation voltage supply lines in claim 1. Also, the Examiner cites lines connected to VEE in Fig. 5 of Komiya as teaching the compensation voltage supply lines in claim 1. However, Applicants respectfully

submit that TFT2 in Fig. 5 of Komiya is connected between the power source PVDD and the EL element, and that the lines connected to VEE are NOT directly connected to either the source or drain terminal of TFT2. Accordingly, Applicants respectfully submit that claim 1 is allowable over the cited references.

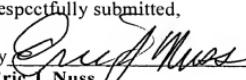
In addition, the Examiner cites TFT3 in Fig. 5 of Komiya as teaching a bias switch, connected between the N-1th compensation voltage supply line and a control terminal of the driving TFT2 connected to the Nth compensation voltage supply line in claim 1. However, Applicants respectfully submit that TFT3 in Fig. 5 of Komiya is connected between VEE and a node between the drain terminal of TFT2 and the EL element, none of which is connected to a control terminal or a gate terminal of the driving transistor. Nor does TFT3 in Fig. 5 of Komiya apply a bias voltage to TFT2 when a scan pulse is supplied to the N-1th gate line. Accordingly, Applicants respectfully submit that claim 1 is allowable over the cited references.

Applicants believe the application is in condition for allowance and early, favorable action is respectfully solicited. If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. A duplicate copy of this sheet is enclosed.

Dated: 3 December 2007

Respectfully submitted,

By   
Eric J. Nuss

Registration No.: 40,106  
McKENNA LONG & ALDRIDGE LLP  
1900 K Street, N.W.  
Washington, DC 20006  
(202) 496-7500  
Attorneys for Applicant